

AMENDMENTS TO THE SPECIFICATION

Please replace paragraph [0006] with the following amended paragraph:

Figs. 3a and 3b illustrate two additional views of the typical display device as described by Figs. 1 and 2. Fig. 3a is a top view illustrating an ~~openings~~opening for exposing a portion of the device substrate 302, where the sealant is placed, and locations of various organic polymer areas 304 in the relative center portion of the display device. In order to place a sealant between the device substrate 302 and the shield substrate, the organic polymer material on top of the passivation layer in the opening ~~302305~~302305 is removed so that the substrate having the passivation layer ~~302112~~302112 is exposed. When the sealant is put in the opening ~~302305~~302305, certain portion of the sealant will rest on the neighboring and unremoved organic polymer material 304, and the sealant width or sealant gap is shown marked by the distance *g* confined by the two vertical dotted lines. The area with the sealant gap *g* may be referred to as the sealant region. The horizontal dotted line marked *x* shows the plane by which the cross-sectional view of this portion of the EL device is shown by Fig. 3b.

Please replace paragraph [0021] with the following amended paragraph:

Figs. 4a-4c illustrate an example of the shield substrate sealing method according to one example of the present disclosure. Fig. 4a is a top view of the sealant region of an organic EL display device while Fig. 4b and 4c are sectional views thereof. The device substrate 402 comprises the organic EL display device, which comprises, for example, the OLED multi-layer pixel structure as shown in Fig. 1. As shown, instead of a continuous organic polymer layer, the organic polymer layer 404 has stripes of narrow openings ~~402405~~402405 processed thereon, which may be referred to as a slit structure. This can be done by masking out certain areas from polymer formation, provided that this slit structure does not affect the need of the organic polymer for the active pixel regions. The sealant region 403 is located perpendicular to the openings ~~402405~~402405. As

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such, in selected locations of the openings ~~402~~405, the exposed ~~substrate~~-areas of substrate 402
can receive sealant.